GR 99 P 1352

Abstract

Method and circuit arrangement for picture-in-picture
insertion

A method for picture-in-picture insertion is described,
which is distinguished in particular by the fact that
-     the inset pictures are written to a memory device
(2) in a circulating manner under continuously
incremented write addresses,
-     the first and last address of each written-in
inset picture is stored,
-     an overtake signal is formed by comparing the
instantaneous address with the previously stored
address, said signal indicating whether a previous
address has been reached again and, consequently, the
corresponding picture content has been overwritten,
-     by evaluation of the overtake signal, the current
or preceding segment is selected for read-out depending
on whether or not overtaking took place before the
start of the read-out, and
-     the inset picture stored in the selected segment
is read out with continuously incremented read
addresses and is inserted into the main picture. A
corresponding circuit arrangement is also described.

(Figure 1)